

5 a detection means within said receiving means for detecting a wireless signal  
6 targeted for said computer while said computer is in a power-saving mode; and

7 a control means within said power saving mode selection means for exiting said  
8 power-saving mode in response to said detected wireless signal.

1 18. (unchanged) The computer of claim 17, further includes

2 means for disabling at least one power source when said computer is in said  
3 power-saving mode, wherein said receiving means asserts a wake up signal to said control  
4 means to indicate said detected wireless signal is targeted for said computer; and

5 a power management circuit to enable at least one power source, in response to  
6 said asserted wake up signal.

1 19. (unchanged) The computer of claim 17, wherein said receiving means is an option card  
2 coupled to said computer through an option card bus slot.

### REMARKS

Claim 5 has been cancelled. Thus, Claims 1-4, 6-7, 9-11 and 14-19 are currently pending in the present application, of which Claim 1 has been amended.

The contents of Claim 5 have been incorporated within Claim 1, and no new matter has been added.

### Rejection under 35 U.S.C. § 103

Claims 1-7, 9-11 and 13-19 were rejected under 35 U.S.C. § 103(a) as being unpatentable over *Connery et al.* (US 6,311,276). Applicants respectfully traverse such rejection insofar as it might apply to the claims as amended herein.

Amended Claim 1 now recites "wherein said bit sequence includes a request for said computer to exit said power-saving mode" (lines 5-6). On page 5 of the Office Action, the Examiner asserts that the claimed bit sequence is disclosed in col. 5, lines 47-51 of *Connery*. In col. 5, lines 47-51, *Connery* simply discloses that CPU 25 has a reset function and that power management logic 30 may issue a system reset command. However, a reset function and a system reset command, which are well-known to those skilled in the art, are not the same as the claimed "request ... to exit said power-saving mode."

Amended Claim 10 now recites "wherein said receiving means is adapted to regenerate some or all of said bit sequence" (lines 4-5). On page 6 of the Office Action, the Examiner asserts that the claimed receiving means is disclosed in col. 5, lines 25-37 of *Connery*. In col. 5, lines 25-37, *Connery* states:

The secure Wake On LAN network interface card 31 allows the system to receive Wake On LAN packets across the medium 33, and in response to issue signals to the power management circuitry 30, which results in waking up the CPU, or otherwise bringing up the system to allow functions specified by the network management system to be performed. Thus, an information system department using the management station is able to do end node management, such as software updates, backups of data, and other system wide services in the network, even in the presence of sleeping green PCs.

There is no teaching or suggestion from the above-mentioned passage regarding the claimed receiving means adapted to regenerate some or all of said bit sequence.

Amended Claim 17 now recites "a power saving mode selection means for selectively entering and exiting a power-saving mode" (lines 3-4). On page 9 of the Office Action, the Examiner asserts that the claimed power saving mode selection means is disclosed in col. 5, lines 26-32 of *Connery*. In col. 5, lines 26-32, *Connery* states:

The secure Wake On LAN network interface card 31 allows the system to receive Wake On LAN packets across the medium 33, and in response to issue signals to the power management circuitry 30, which results in waking up the CPU, or otherwise bringing up the system to allow functions specified by the network management system to be performed.

There is no teaching or suggestion from the above-mentioned passage regarding the claimed power saving mode selection mode means. Because the claimed invention includes novel features that are not taught or suggested by *Connery*, the § 103 rejection is believed to be overcome.

### CONCLUSION

Claims 1-4, 6-7, 9-11 and 14-19 are currently pending in the present application.

For the reasons stated above, Applicants believe that independent Claims 1, 10 and 17 along with their respective dependent claims are in condition for allowance. The remaining prior art cited by the Examiner but not relied upon has been reviewed and is not believed to show or suggest the claimed invention.

No fee or extension of time is believed to be necessary; however, in the event that any fee or extension of time is required for the prosecution of this application, please charge it against Deposit Account No. 50-0563.

Respectfully submitted,



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## IN THE CLAIMS

1. (Twice amended) A method for receiving a wireless signal by a computer adapted to operate in a power-saving mode, said method comprising the steps of:

detecting within a computer a wireless signal representing a bit sequence when said computer is operating in a power-saving mode, wherein said wireless signal is targeted for said computer, wherein said bit sequence includes a request for said computer to exit said power-saving mode;

exiting said power-saving mode automatically in response to said wireless signal;

regenerating some or all of said bit sequence of said wireless signal; and

storing said some or all of said bit sequence of said wireless signal in a memory after exiting said power-saving mode.

2. (unchanged) The method of claim 1, further includes the steps of:

determining whether a wireless signal receiver device is installed and enabled by reading a plurality of status signals; and

exiting said power-saving mode only if said wireless signal receiver device is installed and enabled.

3. (unchanged) The method of claim 1, wherein said detecting further includes detecting a particular identification tag embedded in said bit sequence.

4. (unchanged) The method of claim 1, wherein wireless signal is transmitted through a radio frequency channel.

5. cancelled

6. (unchanged) The method of claim 1, wherein said bit sequence includes a request to continue execution of a program that is suspended while said computer is in said power-saving mode.

7. (unchanged) The method of claim 1, wherein said computer comprises a receiving means for detecting said wireless signal, and said computer further comprises a switch for maintaining power to said receiving means while operating in power-saving mode, and further comprising the step of:

setting said switch to maintain power to said receiving means prior to entering said power-saving mode.

9. (unchanged) The method of claim 1, further includes the steps of:

processing information conveyed by said bit sequence; and

returning to said power-saving mode.

10. (unchanged) A computer for receiving a wireless signal while in a power-saving mode, said computer comprising:

a receiving means adapted to detect a wireless signal representing a sequence of bits, wherein said receiving means is adapted to regenerate some or all of said bit sequence, wherein said wireless signal is targeted for said computer;

a power-saving mode control means adapted to exit said power-saving mode in response to a detection of said wireless signal when said computer is in said power-saving mode;

a switch for enabling power to said receiving means when said computer is in said power-saving mode; and

a memory for storing said some or all of said regenerated bit sequence after said computer has exited said power-saving mode.

11. (unchanged) The computer of claim 10, further includes:

one or more status indicators for indicating whether said receiving means is installed and enabled; and

wherein said power-saving mode control is adapted to exit said power-saving mode, only if said one or more status indicators show that said receiving means is installed and enabled.

14. (unchanged) The computer of claim 10, wherein said receiving means is an optional attachment to said computer.

15. (unchanged) The computer of claim 10, wherein said receiving means is formed in a device bay cover.

16. (unchanged) The computer of claim 15, wherein said device bay cover is an optional attachment to said computer.

17. (unchanged) A computer, comprising:

a receiving means for receiving a signal representing a bit sequence;

a power saving mode selection means for selectively entering and exiting a power-saving mode; and

a detection means within said receiving means for detecting a wireless signal targeted for said computer while said computer is in a power-saving mode; and

a control means within said power saving mode selection means for exiting said power-saving mode in response to said detected wireless signal.

18. (unchanged) The computer of claim 17, further includes

means for disabling at least one power source when said computer is in said power-saving mode, wherein said receiving means asserts a wake up signal to said control means to indicate said detected wireless signal is targeted for said computer; and

a power management circuit to enable at least one power source, in response to said asserted wake up signal.

19. (unchanged) The computer of claim 17, wherein said receiving means is an option card coupled to said computer through an option card bus slot.